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(54) **METHOD AND APPARATUS FOR CONTROLLING POWER CONSUMPTION OF AN INTEGRATED CIRCUIT**

6,067,627 A * 5/2000 Reents 713/324

* cited by examiner

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(57) **ABSTRACT**

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A method and apparatus for controlling power consumption of an integrated circuit include processing that begins by producing a system clock from a reference clock based on a system clock control signal. The reference clock may be generated from an external crystal oscillator circuit operable to produce a reference clock at a desired frequency. The processing continues by regulating at least one supply from a power source and an inductor based on a power supply control signal. The processing continues by producing the system clock control signal and the power supply control signal based on a processing transfer characteristic of a computational engine and processing requirements associated with processing at least a portion of an application by the computational engine.

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(52) **U.S. Cl.** **365/227; 713/320; 713/322**

(58) **Field of Search** **365/226, 227, 365/228, 229; 713/320, 322**

(56) **References Cited**

U.S. PATENT DOCUMENTS

5,825,674 A * 10/1998 Jackson 713/321

22 Claims, 5 Drawing Sheets

